

United States Patent Application for:

SEQUENTIAL DEPOSITION PROCESS FOR GAP FILLING

Inventors:

- 1) Hung-Tien Yu
a citizen of the Republic of China,
3, 98 Lane
Dong-Shang II Section Rd.
Dong-Shang, I-Lan, Taiwan, R.O.C.
- 2) Yiwen Chen,
a citizen of the Republic of China,
2F-1, 68, Ta-Hsueh Rd.,
Hsin-Chu, Taiwan, R.O.C.

Assignee: Applied Materials, Inc.
Entity: Large Entity
Docket No.: 005552 USA/AMT

I hereby certify that this correspondence is being deposited with the United States Postal Service as express mail in an envelope addressed to:

Box Patent Application
Commissioner for Patents
Washington, D.C. 20231

Express Mail Receipt No. EL453088835US Date of Deposit July 12, 2001

By Rowena Montoya Signature Rowena Montoya

DEPOSITION METHOD FOR FILLING RECESSES IN A SUBSTRATE

BACKGROUND

[0001] Embodiments of the present invention relate to a deposition process for filling recesses in a substrate.

[0002] Electronic circuitry, such as integrated circuits, is formed by the deposition, formation (such as by oxidation, nitridation, etc.) and partial removal of layers on a substrate. The substrate may be a silicon wafer (although other materials such as gallium arsenide or glass can be used) onto which various layers may be deposited, removed, or partially removed. A substrate can be a single material but is more often a base material having situated thereon one more conducting, insulating (dielectric) or semiconducting materials.

[0003] The partial removal of a material on a substrate via reactive gases, commonly known as etching, begins with a step in which a patterned photoresist or mask layer is formed on the substrate by a conventional photolithographic method. In the following etching step, the substrate is placed in a chamber and exposed to an energized plasma of a gas that is energized by, for example, microwave energy or radio frequency energy. A biasing electrical voltage may be coupled to the energized gas so that charged species (reactive ions) within the gas are energized toward the substrate. In the etching method, recesses shaped as narrow channels, holes, or trenches, are formed in the substrate.

[0004] After etching, deposition methods are used to fill the etched recesses, for example, with a dielectric or conducting material. However, in some conventional recess-filling methods, voids are often formed within the material being deposited in the recess during the recess-filling process. The voids are especially prone to being formed when the sidewalls of the recesses are not smooth which arise from the narrow widths of the recesses. In the deposition process, overhangs that form on the non-smooth recess sidewalls often coalesce to form a void in the recess.

09505053 071201

[0005] In a "bottom-up" deposition method, the recesses are attempted to be filled without voids, by maintaining a higher deposition rate at the bottom of the recess relative to the deposition rate on the sidewalls of the recesses. For example, when silicon oxide is deposited in a recess, the deposition rate of the silicon oxide on underlying silicon material is higher than the deposition rate of the silicon oxide on underlying silicon nitride material, as for example, shown in Figure 2. The higher oxide deposition rates on silicon reduce the formation of overhangs during filling of the recess, especially when the silicon material is at the bottom of the recess and the nitride material is on the sidewalls of the recess. For example, the bottom-up deposition method is often applied in shallow trench isolation processes. In such methods, a precursor deposition gas, such as a combination of O_3 (ozone) and TEOS (tetraethylorthosilicate) having a high ratio of O_3 to TEOS is used to enhance the selectivity of the deposition growth rates for both nitride and thermal oxide. The higher the ratio of O_3 to TEOS the higher is the deposition rate selectivity ratio, which in turn, affects the recess-filling capability.

[0006] However, when the bottom-up method is used to fill recesses 27 in a substrate 20, some recesses 27 are easily filled but others are not, even when the recesses 27 are not very narrow, as for example, illustrated in Figure 1. In this substrate 20, a plurality of recesses 27, such as trenches, are etched between polysilicon gates 22 on the substrate 20. The sidewalls of the polysilicon gates 22 are covered with nitride spacers 24. A conformal nitride liner 26 covers the nitride spacers 24, polysilicon gates 22, and other portions of the substrate, such as the bottom of the recesses 27. After an oxide layer 28 is formed to fill the recesses 27, some of the recesses 27 are not easily filled, and voids 30 are still formed in these recesses 27.

[0007] Generally, the recesses 27 which are not easily filled without forming voids 30 are those which have non-smooth profiles with some reentrant cavities 10 thereon, as shown in Figure 3. To find a method for filling these recesses 27, precursor gas compositions having different O_3 concentrations, and different process temperatures were tried. Although using different process

0905053.071201

parameters was determined to improve recess filling a little, the voids 30 were still formed in some of the recesses 27 because of their non-smooth profiles. For example, voids 30 were formed even in the small tapered recesses 27 when the sidewalls of the tapered recesses 27 had the reentrant cavities 10.

[0008] Thus, it is desirable to have a deposition method that is capable of filling recesses, such as trenches, in a substrate, without forming voids in the recesses.

SUMMARY

[0009] A deposition method for filling recesses in a substrate comprises exposing the substrate to an energized deposition gas comprising first and second components, to deposit a first layer of a material in the recess, and thereafter, reducing the ratio of the first component to the second component, to deposit a second layer of the material over the first layer in the recess.

[0010] Another deposition method for filling recesses in a substrate, comprises providing a substrate having recesses; exposing the substrate to an energized deposition gas comprising a first volumetric flow ratio of O_3 and TEOS, to deposit a first layer of silicon oxide material in the recess; and reducing the volumetric flow ratio of the O_3 to the TEOS, to deposit a second layer of silicon oxide material over the first layer in the recess.

[0011] The deposition method may be used to fill recesses on a substrate that are between polysilicon gates and have sidewalls covered with silicon nitride spacers. The silicon nitride spacers, polysilicon gates and the other portions of the substrate, are covered with a silicon nitride liner. The method comprises providing an energized deposition gas comprising O_3 and TEOS, to form a first layer of silicon oxide in the recess; and reducing the volumetric flow ratio of O_3 to the TEOS in the deposition gas, to fill the recesses with silicon oxide after the first layer is formed.

09905053 071201

DRAWINGS

[0012] Figure 1 (prior art) is a schematic cross-sectional view of a substrate having recesses filled by a conventional recess-filling method;

[0013] Figure 2 (prior art) is a graph showing a comparison of the deposition rates of silicon oxide material on underlying silicon or silicon nitride;

[0014] Figure 3 (prior art) is a schematic cross-sectional view of a profile of a recess having reentrant cavities; and

[0015] Figures 4A to 4C are schematic cross-sectional views of a substrate having recesses that are filled by deposition method according to an embodiment of the present invention.

DESCRIPTION

[0016] An exemplary embodiment of an application of a deposition method according to the present invention, to fill recesses 107 in a substrate 100, is illustrated in Figures 4A to 4C. While the present method is illustrated with respect to an exemplary substrate embodiment, it should be noted that the method may be used on other types of substrates 100, as would be apparent to one of ordinary skill in the art. Referring to Figure 4A, an exemplary substrate embodiment, has a plurality of polysilicon gates 102 formed in the substrate 100 and the sidewall portions of the polysilicon gates 102 are covered with silicon nitride spacers 104. A liner 106 comprising a thin layer of silicon nitride material is formed substantially conformal to and on the nitride spacers 104, polysilicon gates 102, and other portions of the substrate 100. Note that the liner 106 often has reentrant cavities thereon (as for example illustrated in Figure 3). The recesses 107 are generally between the polysilicon gates 102.

[0017] Referring to Figure 4B, in a first stage, a first layer 108 is deposited in the recesses 27 and over the substrate 100. In this stage, a deposition gas composition that provides different deposition rates depending

upon the underlying material upon which the deposition occurs, is used. In one version, the deposition gas comprises an oxygen-containing compound, such as one or more of O_3 or TEOS. A suitable deposition gas composition comprises first and second components, such as O_3 and TEOS, in a volumetric flow ratio that provides different deposition rates on silicon or silicon nitride material. The deposition gas, when energized to form a plasma, deposits a first layer 108 of silicon oxide on the liner 106. The first layer 106 is formed using the bottom-up growth mechanism for smoothening the profile of the recesses 107 by filling any reentrant cavities. However, The recesses 107 are not entirely filled in this step and other sequential deposition steps may be used to fill the recesses 107.

[0018] The reentrant cavities in the liner 106 are smoothened out because the deposition gas of O_3 and TEOS provides higher deposition rates of oxide material on the sidewall portions of the recesses 27 than on the bottom portions. As shown in Figure 4A, the thickness of silicon nitride material on the sidewalls of the recesses 107 (the oxide spacers 104 plus the nitride liner 106) is substantially thicker than the thickness of the silicon nitride material on the bottoms of the recesses 107 (only the nitride liner 106). Therefore, in the beginning, the reentrant cavities of the nitride liner 106 on the sidewalls of the recesses are gradually smoothened out by the deposition of the first oxide layer 108. Typically, a thickness of the first oxide layer 108 that is suitable for smoothening the reentrant cavities in the nitride liner 106 is from about 200 to about 800 angstroms.

[0019] Referring to Figure 4C, after deposition of the first layer 108, the process conditions are changed in a second process stage to deposit a second layer 110, also of silicon oxide material, to fill the recesses 107. In the second stage, deposition of the oxide material is continued, but the ratio of O_3 to TEOS in the deposition gas is gradually reduced, to deposit additional oxide material in a second layer 110 that fills the recesses 107. Note that the overall deposition rate of a deposition gas having a high ratio of O_3 :TEOS is quite slow, and therefore, the ratio of O_3 to TEOS is gradually reduced in the second stage to also increase the deposition rate. In the ratio-reducing step, the oxide deposition rate on the sidewalls of each recess 107 gradually matches up to the deposition rate obtained

09905053 071201

on the bottom of the recesses 107. The ratio-reducing step is typically performed for about 30 seconds after the reentrant cavities have been smoothened out.

[0020] The recesses 107 may also be filled by performing a first stage that uses a deposition gas having a high ratio of O_3 :TEOS followed by a second stage that uses a deposition gas having a low ratio of O_3 :TEOS. However, this method may not be as desirable because the profiles of the recesses 107 that were filled using such first and second stages were generally too small to be fully filled in the second stage, and thus, some voids were formed in the recesses 107 during the second stage. The recesses were narrower for the second stage because they are partially filled with the oxide material deposited formed in the first stage. Such a two-stage method is often better performed by re-initializing the chamber after the first stage, for example, by re-introducing a deposition gas comprising O_3 and TEOS into the chamber.

[0021] While the composition of the deposition gas may be changed gradually or in a step-wise manner; it is generally preferred to continuously introduce the deposition gas into the chamber while gradually changing the composition of the deposition gas, for example, by reducing the ratio of O_3 to TEOS in the deposition gas while it is flowing into the chamber. The reduction may be done by, for example, reducing the flow rate of O_3 or increasing the flow rate of TEOS. A gradual change in deposition conditions reduces the formation of interfaces between abutting oxide layers within the deposited oxide material that may otherwise form when a step-wise method having distinct stages is used. Such interfaces are undesirable because they are often a source of voids. The interfaces are clearly visible under a microscope after a specimen is dipped in an acid bath. An acid dip may also be performed to verify the quality of the recess filling process.

[0022] As shown in Figure 4, the recesses 107 filled by the present method have substantially no voids therein. Moreover, the narrowest recess 107 which can be filled by the present invention, as for example, illustrated in the leftmost recess of Figure 4C, has a width of about 0.08 micrometers. Furthermore, according to the present invention, the recesses having different widths can be simultaneously filled.

005552 USA/AMT

[0023] It is noted that the invention has been described in detail herein with reference to recesses between polysilicon gates in a substrate comprising a silicon semiconductor wafer; however, it is to be understood that this description is by way of example only, and is not to be construed to limit the present invention. For example, a method according to the present invention may also be used to fill recesses for shallow trench isolation, without departing from the spirit and scope of the present invention. Thus, although the invention has been described in detail herein with reference to its preferred embodiment, it is to be understood that numerous changes in the details of the embodiments of the invention, and additional embodiments of the invention, will be apparent to, and may be made by, persons of ordinary skill in the art having reference to this description. It is contemplated that such changes and additional embodiments are within the spirit and scope of the invention as claimed below.

09905053 071204